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TRANSMITTAL FORM (to be used for all correspondence during pendency of filed application)		Application Number	09/748,098
		Filing Date	December 21, 2000
		First Named Inventor	Nicholas J. Kelsey
		Group Art Unit Number	2183
		Examiner Name	David J. Huisman
Total Number of Pages in This Submission	26	Attorney Docket Number	20880-05093

ENCLOSURES (check all that apply)	
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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.
SERIAL NO.: 09/748,098
FILING DATE: December 21, 2000
TITLE: System and Method for Instruction Level Multithreading in an
Embedded Processor Using Zero-Time Context Switching
EXAMINER: David J. Huisman
GROUP ART UNIT: 2183
ATTY. DKT. NO.: 20880-05093

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Dated: Jan 30, 2006

By: [Signature]

Hector J. Ribera, Reg. No. 54,397

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AMENDMENT E AND RESPONSE UNDER 37 C.F.R. § 1.111

Sir:

This amendment for the patent application identified above is in response to the
Office Action dated September 30, 2005, which set a shortened statutory period for response
that expires on December 30, 2005.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 13 of this paper.

Kindly amend this application as indicated herein.

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